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Substitution for form 1449A/PTO <h1>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h1> (Use as many sheets as necessary)				Complete if Known	
				Application Number	09/085,755
				Filing Date	May 27, 1998
				First Named Inventor	Frampton ELLIS, III
				Art Unit	2153
				Examiner Name	Aaron STRANGE
				Attorney Docket Number	313449-P0005 P1
Sheet	1	of	2		

[illegible]

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁸
		Country Code ² Number ³ Kind Code ⁵ (if known)				
Examiner Signature				Date Considered		

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Notice of Allowance issued in U.S. Patent Application No. 09/884,041, mailed October 9, 2008.	
		Office Action issued in U.S. Patent Application No. 10/663,911, mailed September 26, 2008	
		A. KAYSSI et al., "FPGA-based Internet Protocol Firewall Chip", Electronics, Circuits and Systems, 2000. ICECS 2000. The 7th IEEE International Conference on Volume 1, pp. 316-319, December 17-20, 2000.	
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